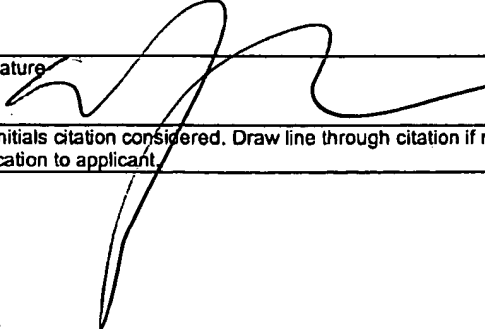
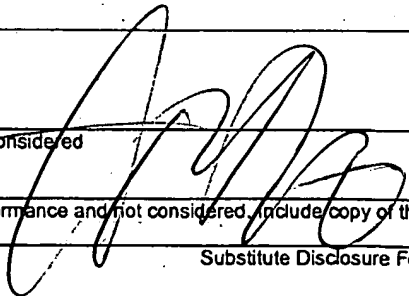


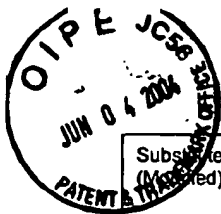
Substitute Form PTO-1449 (Modified) Information Disclosure Statement by Applicant (Use several sheets if necessary) (37 CFR §1.98(b))	U.S. Department of Commerce Patent and Trademark Office	Attorney's Docket No. 10559-306US1	Application No. 107070035
	Applicant Gilbert Wolrich et al.		
	Filing Date February 26, 2002		Group Art Unit

U.S. Patent Documents							
Examiner Initial	Desig. ID	Patent Number	Issue Date	Patentee	Class	Subclass	Filing Date If Appropriate
<i>h</i>	AA	5,428,809	06/27/1995	Coffin et al.			
<i>h</i>	AB	5,517,628	05/14/1996	Morrison et al.			
<i>h</i>	AC	5,659,722	08/19/1997	Blaner et al.			
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	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						

Foreign Patent Documents or Published Foreign Patent Applications								
Examiner Initial	Desig. ID	Document Number	Publication Date	Country or Patent Office	Class	Subclass	Translation	
							Yes	No
	AL							
	AM							
	AN							
	AO							
	AP							

Other Documents (include Author, Title, Date, and Place of Publication)		
Examiner Initial	Desig. ID	Document
	AQ	
	AR	
	AS	
	AT	

Examiner Signature 	Date Considered 
EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	

Sheet 1 of 2

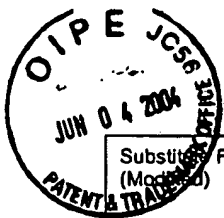
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Information Disclosure Statement by Applicant (Use several sheets if necessary) (37 CFR §1.98(b))		Applicant Gilbert Wolrich et al.	
		Filing Date February 26, 2002	Group Art Unit 2183

Foreign Patent Documents or Published Foreign Patent Applications								
Examiner Initial	Desig. ID	Document Number	Publication Date	Country or Patent Office	Class	Subclass	Translation	
							Yes	No
	AA	WO 01/50679	07/12/2001	WIPO				
	AB	WO 01/50247	07/12/2001	WIPO				
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	AN	WO 94/15287	07/07/1994	WIPO				
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	AQ	EP 0 633 678	01/11/1995	Europe				
	AR	EP 0 464 715	01/08/1992	Europe				
	AS	EP 0 379 709	08/01/1990	Europe				
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Examiner Initial	Desig. ID	Document
	AU	Agarwal et al., "April: A Processor Architecture for Multiprocessing," Proceedings of the 17 th Annual International Symposium on Computer Architecture, IEEE, pp. 104-114.
	AV	Byrd et al., "Multithread Processor Architectures," <i>IEEE Spectrum</i> , Vol. 32, No. 8, New York, 1 August 1995, pp. 38-46.
	AW	Chang et al., "A New Mechanism For Improving Branch Predictor Performance," IEEE, pp. 22-31 (1994).

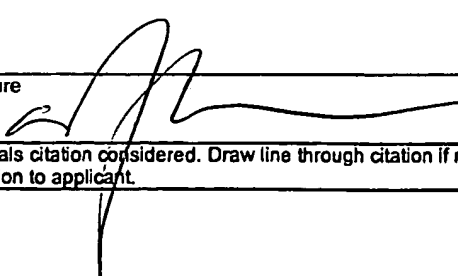
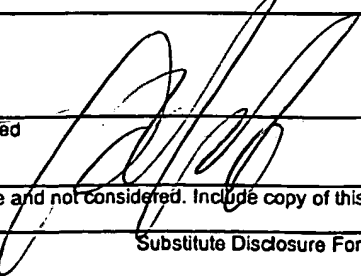
Examiner Signature 	Date Considered
EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	

Substitute Disclosure Form (PTO-1449)

Sheet 2 of 2

Substitute Form PTO-1449 (Mockup)	U.S. Department of Commerce Patent and Trademark Office	Attorney's Docket No. 10559-306US1	Application No. 10/070,035
Information Disclosure Statement by Applicant (Use several sheets if necessary) (37 CFR §1.98(b))		Applicant Gilbert Wolrich et al.	
		Filing Date February 26, 2002	Group Art Unit 2183

Other Documents (include Author, Title, Date, and Place of Publication)		
Examiner Initial	Desig. ID	Document
n	AX	Doyle et al., <i>Microsoft Press Computer Dictionary</i> , 2 nd ed., Microsoft Press, Redmond, Washington, USA, 1994, p. 326.
n	AY	Farkas et al., "The multicluster architecture: reducing cycle time through partitioning," IEEE, vol. 30, December 1997, pp. 149-159.
n	AZ	Fillo et al., "The M-Machine Multicomputer," IEEE Proceedings of MICRO-28, 1995, pp. 146-156.
n	AAA	Gomez et al., "Efficient Multithreaded User-Space Transport for Network Computing: Design and Test of the TRAP Protocol," <i>Journal of Parallel and Distributed Computing</i> , Academic Press, Duluth, Minnesota, USA, vol. 40, no. 1, 10 January 1997, pp. 103-117.
n	ABB	Haug et al., "Reconfigurable hardware as shared resource for parallel threads," IEEE Symposium on FPGAs for Custom Computing Machines, 1998.
n	ACC	Hauser et al., "Garp: a MIPS processor with a reconfigurable coprocessor," Proceedings of the 5 th Annual IEEE Symposium on Field-Programmable Custom Computing Machines, 1997.
n	ADD	Hennessy et al., "Computer Organization and Design: The Hardware/Software Interface," Morgan Kaufman Publishers, 1998, pp. 476-482.
n	AEE	Hyde, R., "Overview of Memory Management," <i>Byte</i> , vol. 13, no. 4, 1998, pp. 219-225.
n	AFF	Intel, "IA-64 Application Developer's Architecture Guide," Rev.1.0, May 1999, pp. 2-2, 4-29 to 4-31, 7-116 to 7-118 and c-21.
n	AGG	Keckler et al., "Exploiting fine grain thread level parallelism on the MIT multi-ALU processor," IEEE, June 1998.
n	AHH	Litch et al., "StrongARMing Portable Communications," IEEE Micro, 1998, pp. 48-55.
n	AII	Mendelson et al., "Design Alternatives of Multithreaded Architecture," <i>International Journal of Parallel Programming</i> , vol. 27, no. 3, Plenum Press, New York, USA, June 1999, pp. 161-193.
n	AJJ	Schmidt et al., "The Performance of Alternative Threading Architectures for Parallel Communication Subsystems," Internet Document, <i>Online!</i> , 13 November 1998.
n	AKK	Thistle et al., "A Processor Architecture for Horizon," IEEE, 1998, pp. 35-41.
n	ALL	Tremblay et al., "A Three Dimensional Register File for Superscalar Processors," IEEE Proceedings of the 28 th Annual Hawaii International Conference on System Sciences, 1995, pp. 191-201.
n	AMM	Trimberger et al., "A time-multiplexed FPGA," Proceedings of the 5 th Annual IEEE Symposium on Field-Programmable Custom Computing Machines, 1998.
n	ANN	Turner et al., "Design of a High Performance Active Router," Internet Document, <i>Online</i> , 18 March 1999.
n	AOO	Vibhatavanijt et al., "Simultaneous Multithreading-Based Routers," Proceedings of the 2000 International Conference of Parallel Processing, Toronto, Ontario, Canada, 21-24 August 2000, pp. 362-359.
n	APP	Wadler, "The Concatenate Vanishes," University of Glasgow, December 1987 (revised November 1989), pp. 1-7.
n	AQQ	Wazlowski et al., "PRSIM-II computer and architecture," IEEE Proceedings, Workshop on FPGAs for Custom Computing Machines, 1993.

Examiner Signature 	Date Considered 
EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	

Substitute Disclosure Form (PTO-1449)

ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

12/03/04

Title of Invention

BRANCH INSTRUCTIONS IN A MULTITHREADED PARALLEL
PROCESSING SYSTEM

Application Number : 10/070035
Confirmation Number: 9914
First Named Applicant: Gilbert Wolrich
Attorney Docket Number: 10559-306US1
Art Unit:
Examiner:
Search string:



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or 6289011 or 6298370 or 6307789 or 6324624 or 6345334 or 6347344 or 6351808
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or 6009505).pn

US Patent Documents

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

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W	2	6223279	2001-04-24	Nishimura et al.			
W	3	6247025	2001-06-12	Bacon			
W	4	6256713	2001-07-03	Audityan et al.			
W	5	6272616	2001-08-07	Fernando et al.			
W	6	6275505	2001-08-14	O Loughlin et al.			
W	7	6279113	2001-08-21	Vaidya			
W	8	6289011	2001-09-11	Seo et al.			
W	9	6298370	2001-10-02	Tang et al.			
W	10	6307789	2001-10-23	Wolrich et al.			
W	11	6324624	2001-11-27	Wolrich et al.			
W	12	6345334	2002-02-05	Nakagawa et al.			
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W	14	6351808	2002-02-26	Joy et al.			
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W	17	6360262	2002-03-19	Guenthner et al.			
W	18	6389449	2002-05-14	Nemirovsky et al.			

SP n/h/04

12/03/04

✓	19	6393483	2002-05-21	Latif et al.			
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✓	21	6426940	2002-07-30	Seo et al.			
✓	22	6427196	2002-07-30	Adiletta et al.			
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✓	29	6577542	2003-06-10	Wolrich et al.			
✓	30	6584522	2003-06-24	Wolrich et al.			
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✓	33	6625654	2003-09-23	Wolrich et al.			
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✓	40	6681300	2004-01-20	Wolrich et al.			
✓	41	6694380	2004-02-17	Wolrich et al.			
✓	42	4023023	1977-05-10	Bourrez et al.			
✓	43	5113516	1992-05-12	Johnson			
✓	44	5363448	1994-11-08	Koopman Jr. et al.			
✓	45	5436626	1995-07-25	Fijiwara			
✓	46	5600812	1997-02-04	Park			
✓	47	5652583	1997-07-29	Kang			
✓	48	5717760	1998-02-10	Satterfield			
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Signature

Examiner Name	Date

ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

12/03/04

Title of Invention

BRANCH INSTRUCTIONS IN A MULTITHREADED PARALLEL
PROCESSING SYSTEM

Application Number : 10/070035
Confirmation Number: 9914
First Named Applicant: Gilbert Wolrich
Attorney Docket Number: 10559-306US1
Art Unit:
Examiner:
Search string:



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or 6223207).pn

US Patent Documents

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

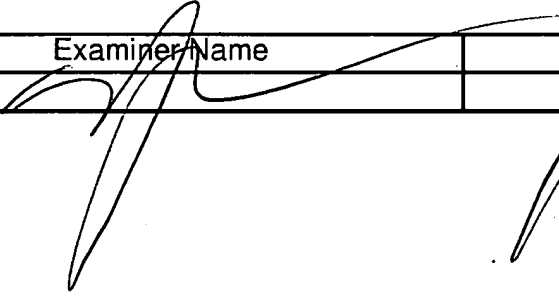
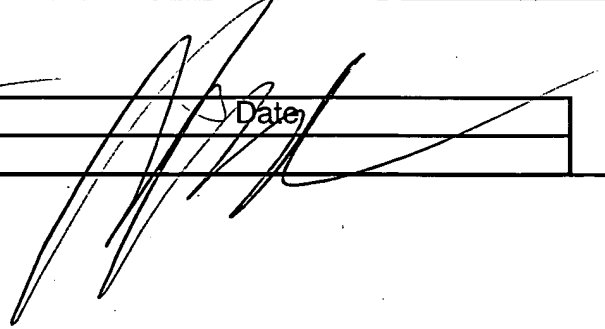
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u	4	5940866	1999-08-17	Chisholm et al.			
u	5	5946487	1999-08-31	Dangelo			
u	6	5948081	1999-09-07	Foster			
u	7	5951679	1999-09-14	Anderson et al.			
u	8	5958031	1999-09-28	Kim			
u	9	5961628	1999-10-05	Nguyen et al.			
u	10	5970013	1999-10-19	Fischer et al.			
u	11	5978838	1999-11-02	Mohamed et al.			
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Signature

Examiner Name	Date
	

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12/03/04

Title of Invention

BRANCH INSTRUCTIONS IN A MULTITHREADED PARALLEL
PROCESSING SYSTEM

Application Number : 10/070035
Confirmation Number: 9914
First Named Applicant: Gilbert Wolrich
Attorney Docket Number: 10559-306US1



Art Unit:

Examiner:

Search string: (5568617 or 5574922 or 5574939 or 5592622 or 5606676 or 5613071 or 5613136 or 5623489 or 5627829 or 5630130 or 5644623 or 5649157 or 5659722 or 5680641 or 5689566 or 5692167 or 5699537 or 5717898 or 5721870 or 5742587 or 5742782 or 5742822 or 5745913 or 5751987 or 5761507 or 5761522 or 5781774 or 5784649 or 5784712 or 5796413 or 5797043 or 5809235 or 5809530 or 5812839 or 5812868 or 5828746 or 5828863 or 5832215 or 5835755 or 5854922 or 5860158 or 5886992 or 5887134 or 5890208 or 5892979 or 5905876 or 5905889 or 5915123 or 5933627 or 5659687).pn

US Patent Documents

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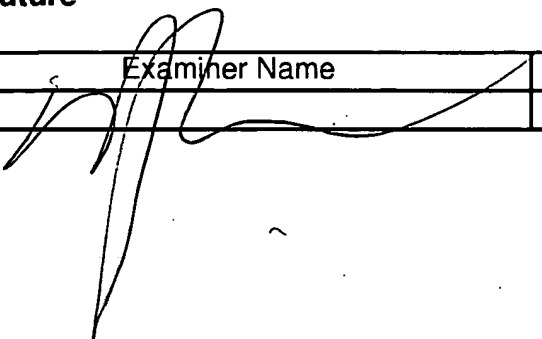
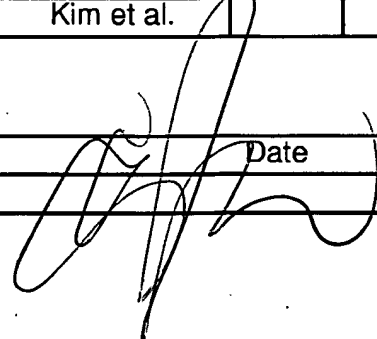
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DR p/m/8

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Signature

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Search string:



(3373408 or 3478322 or 3792441 or 3913074 or 3940745 or 4130890 or 4392758 or 4400770 or 4514807 or 4523272 or 4724521 or 4745544 or 4777587 or 4866664 or 5073864 or 5140685 or 5142683 or 5155831 or 5155854 or 5168555 or 5173897 or 5255239 or 5263169 or 5347648 or 5357617 or 5367678 or 5390329 or 5392391 or 5392411 or 5392412 or 5404464 or 5404482 or 5432918 or 5442756 or 5448702 or 5450351 or 5452437 or 5459842 or 5463625 or 5467452 or 5481683 or 5487159 or 5517628 or 5517648 or 5542070 or 5542088 or 5544236 or 5550816 or 5557766 or 4992934 or 20020053017 or 20020056037 or 20030041228 or 20030145159 or 20030191866 or 20040039895 or 20040054880 or 20040071152 or 20040073728 or 20040073778 or 20040098496 or 20040109369 or 20040205747).pn

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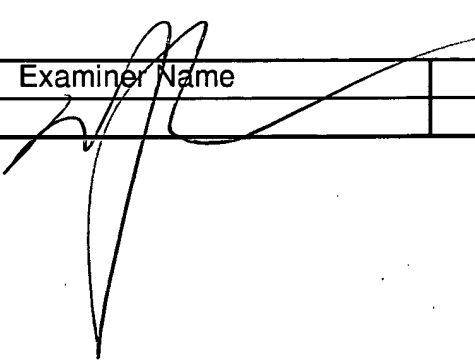
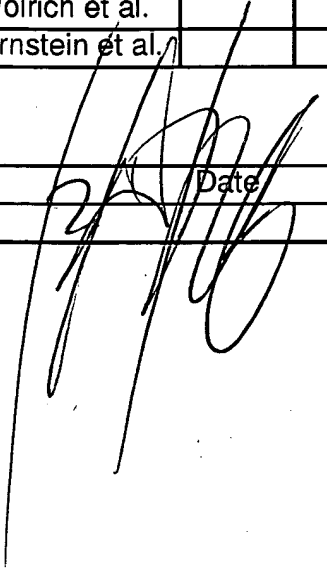
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U.S. Department of Commerce
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10559-306US1Application No.
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(Use several sheets if necessary)

(37 CFR 1.88(b))

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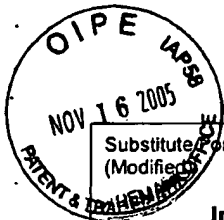
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Substitute Disclosure Form (PTO-1449)

Sheet 1 of 1

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